

	Subclass	ISSUE CLASSIFICATION
	Class	

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PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.C.

PATENT DATE

SCANNED

104 on 5B

APPLICATION NO. 09/979551	CONT/PRIOR D	CLASS 961	SUBCLASS 767	ART UNIT 3837	EXAMINER HOLLINGTON
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Massively parallel interface for electronic circuits

PTO-2040
12/89

ISSUING CLASSIFICATION

[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. _____ (Assistant Examiner) (Date)				NOTICE OF ALLOWANCE MAILED	
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				Amount Due	Date Paid
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